



INFORMATION DISCLOSURE STATEMENT BY APPLICANT (use as many sheets as necessary)		Complete if Known	
		Application Number	09/735,899
		Filing Date	December 14, 2000
		First Named Inventor	JOURDAN et al
		Group Art Unit	2183
		Examiner Name	B.J. O'Brien
Sheet 2	of 2	Attorney Docket Number	2207/9807

U.S. PATENT DOCUMENTS					
Examiner Initials *	Cite No. ¹	U.S. Patent Document Number Kind Code ² (if known)	Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
AL		Friendly et al, "Alternative Fetch and Issue Policies for the Trace Cache Fetch Mechanism", 30th Annual IEEE/ACM Intl. Symposium on Microarchitecture, Research Triangle Park, North Carolina, December 1-3, 1997			
AL		Intrater et al, "Performance Evaluation of a Decoded Instruction Cache for Variable Instruction-Length Computers", 19th Annual Intl. Symposium on Computer Architecture, Gold Coast, Australia, May 19-21, 1992			
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AL		Glaskowsky, Peter N., "Pentium 4 (Partially) Previewed", <i>Microprocessor Report</i> , Vol. 14, Archive 8, pp. 1, 11-13, August 2000			
AL		Papworth, David B., "Tuning the Pentium Pro Microarchitecture", <i>IEEE Micro</i> , IEEE Computer Society, Vol. 16, No. 2, April 1996			
AL		Upton, Michael, "The Intel Pentium® 4 Processor", http://www.intel.com/pentium4 , October 2000			
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Examiner Signature	since li			Date Considered	8-29-2005

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